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# Final CALL FOR PAPERS

# WRTL'T'12

## The Thirteenth Workshop on RTL and High Level Testing

November 22-23, 2012, Toki Messe, Niigata, JAPAN

In conjunction with the 20th Asian Test Symposium (ATS'12) in Niigata

### Sponsored by

IEEE Computer Society Test Technology Technical Council

Technical Committee on Dependable Computing, Information and Systems Society, the IEICE



### SCOPE

The purpose of this workshop is to bring researchers and practitioners of LSI testing from all over the world together to exchange ideas and experiences in register transfer level (RTL) and high level testing. WRTL'T'12, the 13th workshop, will be held in conjunction with the 21th Asian Test Symposium (ATS'12) in Niigata, Japan. We hope and expect this workshop provides an ideal forum for frank discussion on this important topic for the future system-on-a-chip(SoC) devices and 3D ICs. Areas of interest include but are not limited to:

- High level Testing : RTL/Behavior level testing, High level approaches for testing, RTL ATPG, RTL DFT, RTL BIST, High level synthesis for testability, Relationship between RTL and gate level testing, Functional fault modeling,
- High level test bench generation
- 3D IC Testing
- SoC/Noc Testing: Test scheduling, Core testing, Interconnect testing
- Reliable SoC : System level reliability, Self repair, Fault tolerant SoC
- Microprocessor Testing
- Design Verification
- Gate Level Test Related Issues : Low power testing, Test compression, ATPG, DFT, BIST
- Secure Testing
- Hardware Trojan Detection

### SUBMISSIONS

Authors are invited to submit paper proposals for presentation at the workshop. The proposal may be an extended summary (1,000 words) or a full paper and should include: title, full name and affiliation of all authors, 50 words abstract, keywords and the name of contact author. Good papers will be offered to be submitted to the journal of the Institute of Electronics, Information and Communication Engineers (IEICE). All submissions are now to be made electronically through the EasyChair conference system. Detailed instructions for submissions are to be found the website. Electronic submissions in PDF files are strongly recommended. Please visit the following website for submissions.

<https://www.easychair.org/conferences/?conf=wrtl12>

### KEY DATES

**Submission deadline: Sep. 9, 2012**

Notification of acceptance: September 28, 2012

Camera ready due: October 26, 2012

Photocopies of accepted papers will be handed out to the attendees at the workshop site.

### FURTHER INFORMATION

WWW: <http://www.ieee-wrtl.org/2012/> E-mail: [wrtl12@aries30.cse.kyutech.ac.jp](mailto:wrtl12@aries30.cse.kyutech.ac.jp)



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